



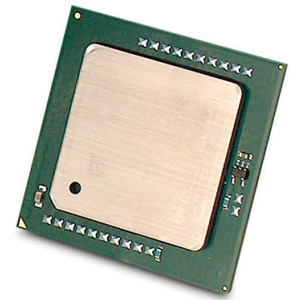
Lenovo Intel Xeon E5-2609 v4 processor 1.7 GHz 20 MB Smart Cache

Brand : Lenovo

Product code: 00YE724

Product name : Intel Xeon E5-2609 v4

Intel Xeon E5-2609 v4, 20M Cache, 1.7 GHz, 6.4 GT/s QPI, Factory Integrated
Lenovo Intel Xeon E5-2609 v4. Processor family: Intel® Xeon® E5 v4, Processor socket: LGA 2011-v3, Processor lithography: 14 nm. Memory channels: Quad-channel, Maximum internal memory supported by processor: 1.54 TB, Memory types supported by processor: DDR4-SDRAM. Market segment: Server, Supported instruction sets: AVX 2.0, Scalability: 2S



Processor		Features	
Processor model *	E5-2609V4	PCI Express slots version	3.0
Processor base frequency *	1.7 GHz	Supported instruction sets	AVX 2.0
Processor family *	Intel® Xeon® E5 v4	Scalability	2S
Processor cores *	8	Physical Address Extension (PAE)	✓
Processor socket *	LGA 2011-v3	CPU configuration (max)	2
Component for	Server/workstation	Embedded options available	✓
Processor lithography *	14 nm	Physical Address Extension (PAE)	46 bit
Processor series	Intel Xeon E5-2600 v4	Processor special features	
Processor threads	8	Intel® Hyper Threading Technology (Intel® HT Technology)	✗
System bus rate	6.4 GT/s	Intel® Identity Protection Technology (Intel® IPT)	✗
Processor operating modes *	64-bit	Intel® Turbo Boost Technology	✗
Processor cache	20 MB	Intel Flex Memory Access	✗
Processor cache type	Smart Cache	Intel® AES New Instructions (Intel® AES-NI)	✓
Thermal Design Power (TDP)	85 W	Enhanced Intel SpeedStep Technology	✓
Stepping	R0	Intel Trusted Execution Technology	✓
Bus type	QPI	Intel VT-x with Extended Page Tables (EPT)	✓
Number of QPI links	2	Intel Demand Based Switching	✓
Memory bandwidth supported by processor (max)	59.7 GB/s	Intel® Secure Key	✓
Memory		Intel TSX-NI	✓
Maximum internal memory supported by processor	1.54 TB	Intel® OS Guard	✓
Memory types supported by processor	DDR4-SDRAM	Intel 64	✓
Memory clock speeds supported by processor	1600,1866 MHz	Intel Virtualization Technology (VT-x)	✓
Memory channels *	Quad-channel	Intel Virtualization Technology for Directed I/O (VT-d)	✓
ECC	✓	Conflict-Free processor	✓
Graphics		Intel® vPro™ Platform Eligibility	✓
On-board graphics card *	✗	Operational conditions	
Features		Tcase	74 °C
Execute Disable Bit	✓		
Idle States	✓		
Thermal Monitoring Technologies	✓		
Market segment	Server		
Maximum number of PCI Express lanes	40		

Disclaimer. The information published here (the "Information") is based on sources that can be considered reliable, typically the manufacturer, but this Information is provided "AS IS" and without guarantee of correctness or completeness. The Information is only indicative and can be changed at any time without notification. No rights can be based on the Information. Suppliers or aggregators of this Information do not accept any liability with regard to the content of (web)pages and other documents, including its Information. The publisher of the Information can not be held liable for the content of 3rd party websites that are linking this Information or are linked to from this Information. You as the User of the Information are solely responsible for the choice and usage of this Information. You are not entitled to transfer, copy or otherwise multiply or distribute the Information. You are obliged to follow the directions of the copyright owner(s) with regard to the use of the Information. Exclusively Dutch law is applicable. With regard to price and stock data on the site, the publisher followed a number of starting points, which are not necessarily relevant for your private or business circumstances. Therefore, the price and stock data are only indicative and are subject to changes. You are personally responsible for the way you use and apply this information. As a user of the Information or sites or documents in which this Information is included, you will adhere to standard fair use including avoidance of spamming, ripping, intellectual-property violations, privacy violations, and any other illegal activity.

Publication date: 27-DEC-2023. Prints or copies of Information are only valid on the printed Publication date