

Features		Logistics data	
Execute Disable Bit	✓	Harmonized System (HS) code	85423119
Idle States	✓	Packaging data	
Thermal Monitoring Technologies	✓	Package type	Retail box
Market segment	Desktop	Technical details	
Use conditions	PC/Client/Tablet	Target market	Gaming
Maximum number of PCI Express lanes	20	Launch date	Q4'21
PCI Express slots version	5.0, 4.0	Status	Launched
PCI Express configurations	1x16+1x4, 2x8+1x4	Weight & dimensions	
Supported instruction sets	SSE4.1, SSE4.2, AVX 2.0	Processor package size	45 x 37.5 mm
Scalability	1S	Other features	
		Maximum internal memory	128 GB



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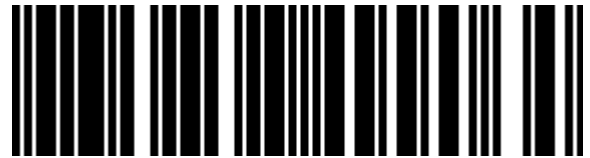
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