

Intel Xeon BX80532KC2000D processor 2 GHz 0.512 MB L2

Brand : Intel

Product family: Xeon

Product code:
NE80532EC041512

Product name :
BX80532KC2000D



Intel® Xeon® Processor 2.00 GHz, 512K Cache, 400 MHz FSB

[Intel Xeon BX80532KC2000D processor 2 GHz 0.512 MB L2:](#)

A bus is a subsystem that transfers data between computer components or between computers. Types include front-side bus (FSB), which carries data between the CPU and memory controller hub; direct media interface (DMI), which is a point-to-point interconnection between an Intel integrated memory controller and an Intel I/O controller hub on the computer's motherboard; and Quick Path Interconnect (QPI), which is a point-to-point interconnect between the CPU and the integrated memory controller.

CPU Cache is an area of fast memory located on the processor. Intel® Smart Cache refers to the architecture that allows all cores to dynamically share access to the last level cache
Intel Xeon BX80532KC2000D. Processor family: Intel® Xeon®, Processor socket: Socket 603, Processor lithography: 130 nm. Market segment: Server, Number of Processing Die Transistors: 55 M, Processing Die size: 131 mm². Package type: Retail box

Processor		Features	
Processor manufacturer *	Intel	Number of Processing Die Transistors	55 M
Processor base frequency *	2 GHz	Processing Die size	131 mm ²
Processor family *	Intel® Xeon®	Embedded options available	✗
Processor cores *	1	Processor special features	
Processor socket *	Socket 603	Intel® Anti-Theft Technology (Intel® AT)	✗
Component for	Server/workstation	Intel® Hyper Threading Technology (Intel® HT Technology)	✓
Processor lithography *	130 nm	Intel® Turbo Boost Technology	✗
Processor threads	2	Intel Clear Video Technology	✗
Processor operating modes *	32-bit	Intel 64	✗
Processor cache	0.512 MB	Intel Virtualization Technology (VT-x)	✗
Processor cache type	L2	Intel Virtualization Technology for Directed I/O (VT-d)	✗
Processor front side bus	400 MHz	Conflict-Free processor	✗
Thermal Design Power (TDP)	58 W	Operational conditions	
Box *	✗	Tcase	70 °C
Cooler included *	✗	Technical details	
VID Voltage Range	1.5 - 1.5 V	Bus bandwidth	400
Bus type	FSB	Bus type units	MHz
CPU multiplier (bus/core ratio)	20	Processor cache	512 KB
Processor codename	Prestonia	Product type	1
Processor code	SL8SE	Status	Launched
Processor ARK ID	27265	Processor brand name	Intel Xeon
Memory		Last change	63903513
ECC	✗	Product family	Legacy Intel Xeon Processor
Graphics		Packaging data	
On-board graphics card *	✗	Package type	Retail box
Features			
Idle States	✗		
Thermal Monitoring Technologies	✗		
Market segment	Server		

Disclaimer. The information published here (the "Information") is based on sources that can be considered reliable, typically the manufacturer, but this Information is provided "AS IS" and without guarantee of correctness or completeness. The Information is only indicative and can be changed at any time without notification. No rights can be based on the Information. Suppliers or aggregators of this Information do not accept any liability with regard to the content of (web)pages and other documents, including its Information. The publisher of the Information can not be held liable for the content of 3rd party websites that are linking this Information or are linked to from this Information. You as the User of the Information are solely responsible for the choice and usage of this Information. You are not entitled to transfer, copy or otherwise multiply or distribute the Information. You are obliged to follow the directions of the copyright owner(s) with regard to the use of the Information. Exclusively Dutch law is applicable. With regard to price and stock data on the site, the publisher followed a number of starting points, which are not necessarily relevant for your private or business circumstances. Therefore, the price and stock data are only indicative and are subject to changes. You are personally responsible for the way you use and apply this information. As a user of the Information or sites or documents in which this Information is included, you will adhere to standard fair use including avoidance of spamming, ripping, intellectual-property violations, privacy violations, and any other illegal activity.

Publication date: 17-JAN-2024. Prints or copies of Information are only valid on the printed Publication date